

# Subhrajit Das | Director's PhD Fellow

Department of Computer Science and Engineering, IIT Gandhinagar, Gujarat, India

☎ +91 XXXXXXXXXX • ✉ subhrajit.das@iitgn.ac.in • 🌐 iamsubhrajit10.me

in subhrajit-das-aaa879157 • 📄 iamsubhrajit10

## Academic Details

<b>PhD in Computer Science and Engineering</b> <i>IIT Gandhinagar, Gujarat, India, Advisors: Prof. Abhishek Bichhawat &amp; Prof. Yuvraj Patel</i>	<b>CPI: 10</b> <i>2025–Present</i>
<b>M.Tech. in Computer Science and Engineering</b> <i>IIT Gandhinagar, Gujarat, India, Specialization: Computer Systems</i>	<b>CPI: 9.84</b> <i>2023–2025</i>
<b>M.Sc. in Computer Science</b> <i>University of Kalyani, West Bengal, India</i>	<b>CGPA: 9.76</b> <i>2021–2023</i>
<b>B.Sc. (Hons.) in Computer Science</b> <i>Panihati Mahavidyalaya (West Bengal State University), West Bengal, India</i>	<b>CGPA: 9.89</b> <i>2018–2021</i>
<b>Class XII in PCM, Computer Science</b> <i>Kalyangarh Vidyamandir (West Bengal Council of Higher Secondary Education), West Bengal, India</i>	<b>86.20%</b> <i>2016–2018</i>

## Projects

**WimpyGPU: Leveraging Commodity Hardware for Compute-intensive GPU Tasks:** IIT Gandhinagar & University of Edinburgh (Oct '25 – present)

Investigating how clusters of commodity hardware can tackle compute-intensive tasks typically reserved for accelerators.  
**Developer Experience of Consent Management Platform Integration:** IIT Gandhinagar (Oct '25 – present)

Investigating the usability challenges developers face when integrating Consent Management Platforms (CMPs) into web applications.

**Accelerating Large Integer Arithmetic with AVX512 [1]:** *M.Tech Thesis*, IIT Gandhinagar (Jan '24 – Apr '25)

Designed high-performance data-parallel algorithms for large integer addition and subtraction using AVX512, achieving average execution-time speedups of  $2.06\times$  and  $2.32\times$  respectively (up to 131k bits) compared to GMP. Additionally, designed a faster Vedic-based multiplication algorithm using AVX512-IFMA for 256-bit operands ( $1.83\times$  speedup) and developed approximate arithmetic variants achieving average speedups of  $2.52\times$  for addition and  $2.80\times$  for multiplication.

**TennisServe: A Parallel Game Matching Server with OpenMP & MPI:** IIT Gandhinagar (Jan '24 – April '24)

Developed a tennis game matching server simulation where multiple players send requests for games. Utilized OpenMP threads and MPI calls to manage client requests and limited tennis court availability.

## Positions of Responsibility

**Teaching Assistant:** IIT Gandhinagar (July '23 – Present)

Assisted with courses such as Distributed Systems and Cloud Computing, Computer & Network Security, Compilers, and Data Structures and Algorithms - I.

**Class Representative:** University of Kalyani (Oct '21 - Jun '23), M.Sc in Computer Science, Batch 2021-2023

## Publications

- [1] Subhrajit Das, Abhishek Bichhawat, and Yuvraj Patel, “DigitsOnTurbo: Leveraging SIMD for Accelerating Large-number Arithmetic,” in *21st European Conference on Computer Systems (EuroSys 2026)*, 2026, Under Review.
- [2] Pratyush Choudhary\*, Subhrajit Das\*, Mukul Paras Potta\*, Prasuj Das, and Abhishek Bichhawat, “Online Authentication Habits of Indian Users,” in *Proceedings of BuildSEC'24, Building a Secure & Empowered Cyberspace*, IEEE Society on Social Implications of Technology (SSIT), New Delhi, India, Dec. 2024.

## Technical Skills

**Languages:** C, C++, Java, Python, Go, R, MATLAB, Bash, Prolog, Kotlin, HTML, CSS, JavaScript, JSP, SQL

**Tools:** Git, Docker, gRPC, OpenMP, MPI, SIMD Intrinsics, Valgrind, PERF, GDB, Wireshark, NS2, Mininet

## Achievements

**Jan 2025:** Selected for the **Director's Fellowship** for admission into the PhD program at IIT Gandhinagar.

**Dec 2024:** Received the **Best Paper Award** for “Online Authentication Habits of Indian Users [2]” at BuildSEC'24.

**2023:** Secured **All India Rank 530** out of 75,680 in **GATE 2023 CSE** with a percentile of 99.30.

**2022 & 2023:** Qualified **UGC NET JRF & WB SET**